



Fig. 1
VCO-1 Schematic: Sheet 1 of 2

- For improved temperature stability, transistors Q5 and Q6 should be expoiced together in thermal contact.
- For even better temperature stability, R8 should be a 2K, +3300ppm/°C thermistor and mounted in close contact with Q5 and Q6.
- Another improvement is to replace Q5 and Q6 with a monolithic matched PNP pair.